



PALM INTRANET

Day : Sunday
Date: 3/6/2005
Time: 18:02:47

Inventor Name Search Result

Your Search was:

Last Name = REUVENI

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09301278	6313683	150	04/28/1999	METHOD OF PROVIDING CLOCK SIGNALS TO LOAD CIRCUITS IN AN ASIC DEVICE	REUVENI, DAVID
60218574	Not Issued	159	07/17/2000	CW900184 DIGITAL CRPLL DATASHEET	REUVENI, DAVID
09821886	Not Issued	094	03/30/2001	DIGITAL CLOCK RECOVERY PPL	REUVENI, DAVID R.
09822041	Not Issued	030	03/30/2001	DIGITAL CLOCK RECOVERY PLL	REUVENI, DAVID R.
09822112	Not Issued	071	03/30/2001	DIGITAL CLOCK RECOVERY PLL	REUVENI, DAVID R.
10146634	Not Issued	030	05/14/2002	DIGITAL MULTI-PHASE CLOCK GENERATOR	REUVENI, DAVID R.
10180634	Not Issued	092	06/26/2002	LOGIC BUILT-IN SELF-TEST (BIST)	REUVENI, DAVID R.
10217051	6567022	150	08/12/2002	MATCHING CALIBRATION FOR DUAL ANALOG-TO-DIGITAL CONVERTERS	REUVENI, DAVID R.
10232968	6667703	150	08/30/2002	GAIN AND OFFSET CALIBRATION FOR MATCHING DIGITAL-TO-ANALOG CONVERTERS	REUVENI, DAVID R.
10271919	6756832	150	10/16/2002	DIGITALLY-PROGRAMMABLE DELAY LINE FOR MULTI-PHASE CLOCK GENERATOR	REUVENI, DAVID R.

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="REUVENI"/>	<input type="text" value="DAVID"/>	

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L1	423	(sample or sampling) same (phases with clock) same (data with clock) same align\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 10:12
L2	260	@ad<"20010330" and 1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/06 10:12